

CLAIMS

There are no amendments to the claims.

A complete listing of all claims ever present in this case in ascending order with status identifier is presented in a separate section.

FEE CALCULATION

Any additional fee required has been calculated as follows:

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	Additional Fee
Total	7	- 20* =		x	0.00
Independent	2	- 3** =		x	0.00
First presentation of Multiple Dependent Claim(s) (if applicable)					
TOTAL					0.00

*not less than 20 ** not less than 3

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge any underpayment or credit any overpayment to Deposit Account No. 50-2215.

CONTINGENT EXTENSION REQUEST

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. § 1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 C.F.R. § 1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 50-2215.

COMPLETE LISTING OF CLAIMS
IN ASCENDING ORDER WITH STATUS INDICATOR

1. (Amended) A dielectric filter comprising:

a dielectric block;

a plurality of conductive through holes arranged in the dielectric block, each of the conductive through holes having an open end along a first surface of the dielectric block;

a respective coupling electrode connected to each conductive through hole, each coupling electrode formed on the first surface of the dielectric block and extended at least to a first edge of the dielectric block, the respective coupling electrodes having a common and continuous, non-conductive gap therebetween shared by the respective coupling electrodes and generating a capacitance therebetween so as to couple the plurality of conductive through holes; and

an outer conductor arranged on outer surfaces of the dielectric block.

2. (Previously Amended) The dielectric filter according to Claim 1, wherein the coupling electrodes further extend onto a second surface of the dielectric block which intersects the first edge of the dielectric block.

3. (Previously Amended) The dielectric filter according to Claim 1, further comprising input/output electrodes arranged on a second surface of the dielectric block and extending from a second edge, opposing the first edge, to generate capacitances between the open ends of the conductive through holes and the input/output electrodes.

4. (Original) A dielectric duplexer comprising a pair of dielectric filters according to Claim 3, one input/output electrode of one filter being usable as a transmission-signal input electrode, one input/output electrode of the other filter being usable as a reception-signal output electrode, and the other respective input/output electrodes of both filters being connected together and to an antenna-connecting electrode.

5. (Original) A communication apparatus comprising a high-frequency circuit and, connected thereto, the dielectric filter according to one of Claims 1 and 2.

6. (Original) A communication apparatus comprising a high-frequency circuit and, connected thereto, the dielectric duplexer according to Claim 4.

7. (Allowed) A dielectric filter comprising:
a dielectric block;
a plurality of conductive through holes arranged in the dielectric block, each of the conductive through holes having a open end along a first surface of the dielectric block;
a respective coupling electrode connected to each conductive through hole, each coupling electrode formed on the first surface of the dielectric block and extended at least to a first edge of the dielectric block, the respective coupling electrodes having a gap therebetween and generating a capacitance therebetween so as to couple the plurality of conductive through holes; and
an outer conductor arranged on outer surfaces of the dielectric block; and
input/output electrodes arranged on a third surface of the dielectric block and extending from a second edge, opposing the first edge, to generate capacitances

between the open ends of the conductive through holes and the input/output electrodes,

wherein the coupling electrodes further extend onto a second surface of the dielectric block which intersects the first edge of the dielectric block.

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